

List of Tables

<i>Table</i>	<i>Title</i>	<i>page</i>
2.1	Summary of cause, effect and design constraint of SHC ripple	14
2.2	Design of Minimum Value of Filter Capacitor	26
2.3	Comparison of Simulation Results of Fig. 2.15 for Different Values of f_v	28
2.4	Summery of Passive and Active Techniques	44
2.5	Summery of Passive and Active Techniques (Continued...)	45
2.6	Summery of Control-oriented Compensation Techniques in DC-AC Converters with Front-end Converter or Control Circuits	46
2.7	Summery of Control-oriented Compensation Techniques in DC-AC Converters with Front-end Converter or Control Circuits (Continued...)	47
3.1	System Parameters	58
3.2	PI Controller Design for 100 Hz Ripple Compensation[Liu and Lai, 2007b]	59
3.3	Comparison of simulation results for the different controllers	62
3.4	Comparison of experimental results	66
4.1	System Parameters	81
4.2	Summary of Experimental Results	88
5.1	System Parameters	98
5.2	Comparison of Simulation Results for Different Values of f_v (At Fixed $f_c = 1500 Hz$)	100
5.3	Comparison of Proposed Control with Existing Control Schemes	106
6.1	Summary of Fig. 6.4	116

